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PATENT

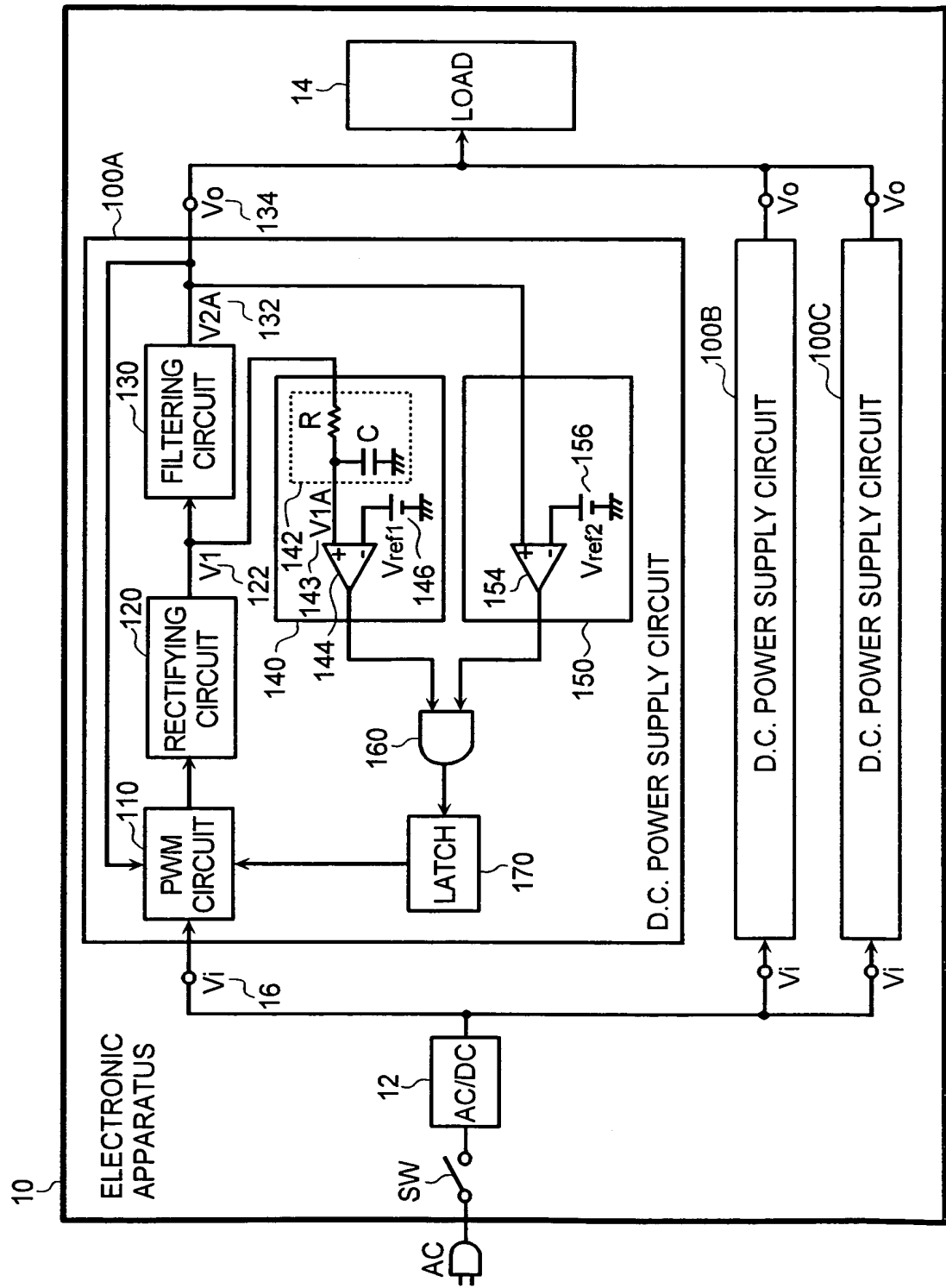
Amendments to the Drawings:

The attached sheets of drawings include formal drawings for Figs. 1-4. These sheets, which include Figs. 1-4, replace the original sheets including Figs. 1-4.

Attachment: Replacement Sheets



FIG.1



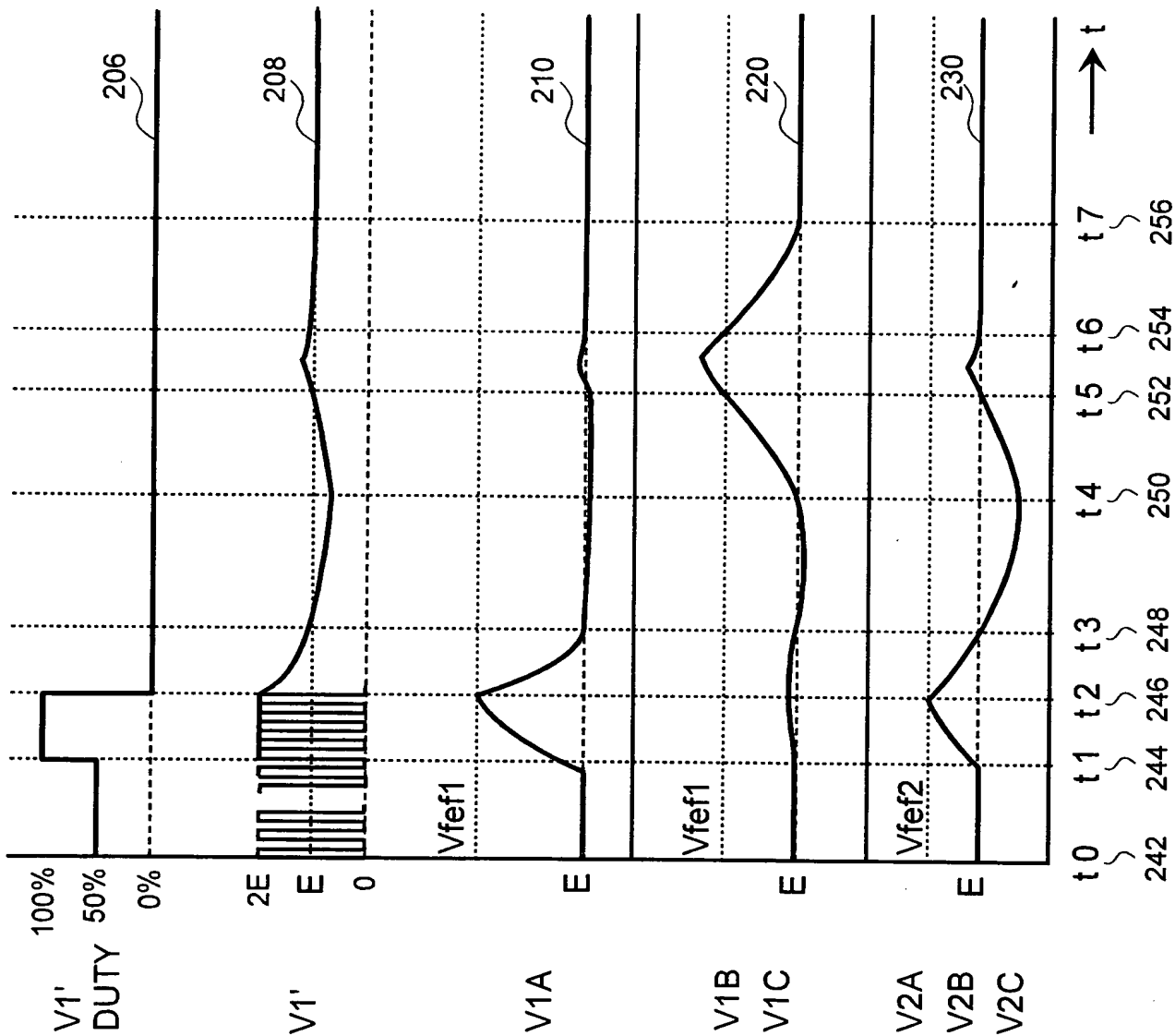
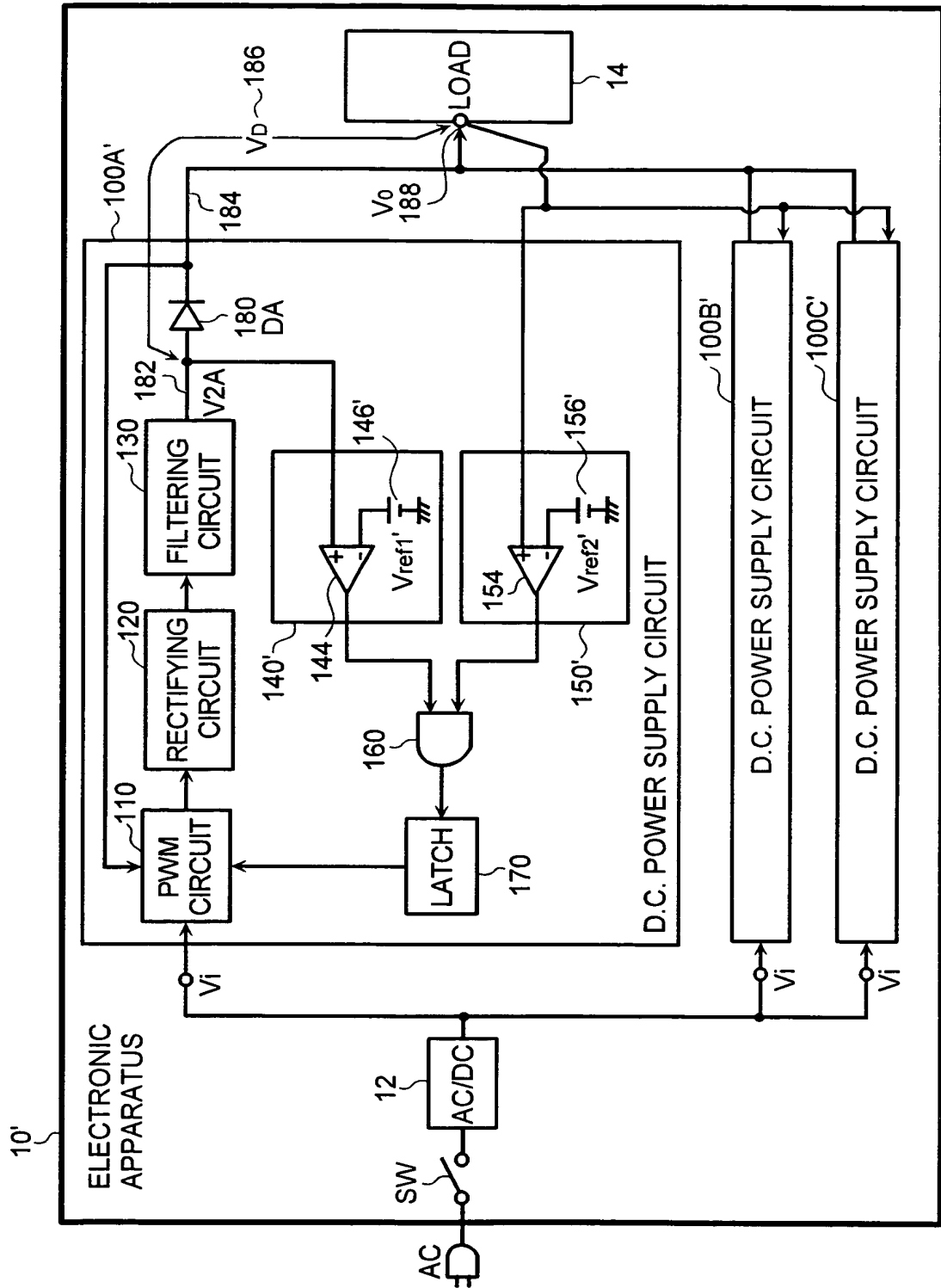


FIG.2



FIG.3



The diagram illustrates an electronic apparatus 10 designed for power conversion. It features an AC input source connected to a switch SW, which then feeds into an AC/DC converter block 12. The output of this converter is distributed to three parallel D.C. power supply circuits, labeled 100A, 100B, and 100C. Each of these circuits is a self-contained unit that begins with a PWM circuit 110, followed by a rectifying circuit 120 and a filtering circuit 130. The output of the filtering circuit is fed back through a network of resistors R1 144 and R2 148 to an error amplifier 140. This amplifier also receives a reference voltage Vref2 150. The output of the error amplifier is connected to a latch 170, which in turn provides a feedback signal to the PWM circuit 110. The final output of circuit 100A is labeled Vo 193 and is connected to an external load 14. The other two circuits, 100B and 100C, provide additional D.C. power supply rails.